SPECIFICATIONS

Customer	
Product Name	Wire Wound SMD Power Inductor
Sunlord Part Number	SWPA4012S Series
Customer Part Number	

 $[\square New Released, \square Revised]$

SPEC No.: SWPA120000

[This SPEC is total 12 pages.] [ROHS Compliant Parts]

Approved By	Checked By	Issued By

Shenzhen Sunlord Electronics Co., Ltd.

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[For Customer approval Only] Date:				
Approved By	Verified By	Re-checked By	Checked By	
Comments:				

[Version change history]

Rev.	Effective Date	Changed Contents	Change Reasons	Approved By
01	1	New release	1	Yabing Yang

Sunlord

4012

1 Scope

This specification applies to the SWPA4012S Series of wire wound SMD power inductor.

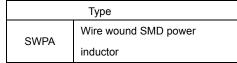
2 Product Description and Identification (Part Number)

1) Description:

SWPA4012S series of Wire wound SMD power inductor.

2) Product Identification (Part Number)

<u>SWPA 4012 S 🔲 🗍 T</u>



Feature type		
S	Standard Type	

Inductance Tolerance		
N ±30%		
М	±20%	

Packing

Tape Carrier Package

Example	Example
1R0	1.0uH
100	10uH
101	100uH

Nominal Inductance

External Dimensions(L×W×H) [mm]

4.0X4.0X 1.2

Special Process code			
Special Process code			
* Standard product is blank			

3 Electrical Characteristics

т

Please refer to Item 12.

- 1) Operating and storage temperature range (individual chip without packing): -40 ~ +125 (Including Self-heating)
- 2) Storage temperature range (packaging conditions): -10 ~+40 and RH 70% (Max.)

4 Test and Measurement Procedures

4.1 Test Conditions

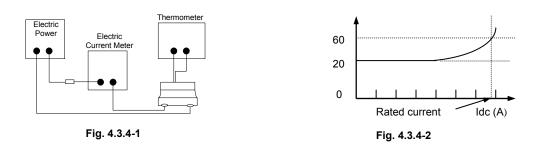
- 4.1.1 Unless otherwise specified, the standard atmospheric conditions for measurement/test as:
 - a. Ambient Temperature: 20±15
 - b. Relative Humidity: 65±20%
 - c. Air Pressure: 86kPa to 106kPa
- 4.1.2 If any doubt on the results, measurements/tests should be made within the following limits:
 - a. Ambient Temperature: 20±2
 - b. Relative Humidity: 65±5%
 - c. Air Pressure: 86kPa to 106kPa

4.2 Visual Examination

Inspection Equipment: visual

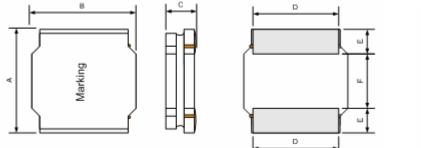
4.3 Electrical Test

- 4.3.1 Inductance (L)
 - a. Refer to Item 12. Test equipment: WK3260B LCR meter or equivalent.
 - b. Test Frequency and Voltage: refers to Item 12.
- 4.3.2 Direct Current Resistance (DCR)
 - a. Refer to Item 12.
 - b. Test equipment: HIOKI 3540 or equivalent.
- 4.3.3 Saturation Current (Isat)
 - a. Refer to Item 12.
 - b. Test equipment: WK3260B LCR meter or equivalent.
 - c. Definition of saturation current (Isat): DC current at which the inductance drops approximate 30% from its value without current.
- 4.3.4 Temperature rise current (Irms)
 - a. Refer to Item 12.
 - b. Test equipment (see Fig. 4.3.4-1, Fig. 4.3.4-2): Electric Power, Electric current meter, Thermometer.
 - c. Measurement method
 - 1. Set test current to be 0 mA.
 - 2. Measure initial temperature of choke surface.
 - 3. Gradually increase current and measure choke temperature for corresponding current.
 - 4. Definition of Temperature rise current: DC current that causes the temperature rise (T =40°C) from 20°C ambient



5 Shape and Dimensions

Dimensions and recommended PCB pattern for reflow soldering, please see Fig.5-1, Fig. 5-2 and Table 5-1.



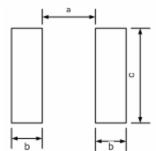
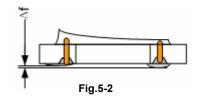


Fig.5-1

[Table 5-1] (Unit: mm)

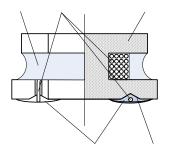
Series	A	В	C.	D	E	F	a.	b.	С
SWPA4012S	4.0±0.2	4.0±0.2	1.2Max.	3.3±0.2	0.95±0.2	2.1±0.2	1.9Тур.	1.1Typ.	3.7Тур.



 Δf : Clearance between terminal and the surface of plate must be 0.1mm max when coil is placed on a flat plate.

6 Structure

The structure of SWPA4012S product, please refer to Fig.6-1 and Table 6-1.



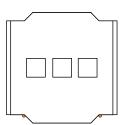
		• •
No.	Components	Material
	Ferrite Core	Ni-Zn Ferrite
	Wire	Polyurethane system enameled copper wire
	Magnetic Glue	Epoxy resin and magnetic powder
	Plating Electrodes	Plating: Ag/Ni/Sn
	Outer Electrodes	Top surface solder coating:Sn96.5%/Ag3.0%/Cu0.5%

Fig. 6-1

7 Product Marking

Please refer to Fig. 7-1.

The content of marking please refers to Item 12.



[Table 6-1]

Reliability T	est	
Items	Requirements	Test Methods and Remarks
8.1 Terminal Strength	No removal or split of the termination or other defects shall occur.	Solder the inductor to the testing jig (glass epoxy board shown in Fig.8.1-1) using eutectic solder. Then apply a force in the direction of the arrow. 10N force. Keep time: 5s
8.2 Resistance to Flexure	Fig.8.1-1 No visible mechanical damage.	Solder the chip to the test jig (glass epoxy board) using eutectic solder. Then apply a force in the direction shown as Fig.8.2-1 . Flexure: 2mm Pressurizing Speed: 0.5mm/sec Keep time: 30±1s Test board size: 100X40X1.0 Land dimension: Please see Fig. 5-1
8.3 Vibration	Fig.8.2-1 No visible mechanical damage. Inductance change: Within ±10%	Solder the chip to the testing jig (glass epoxy board shown as the following figure) using eutectic solder. The chip shall be subjected to a simple harmonic motion having total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 55 Hz. The frequency range from 10 to 55 Hz and return to 10 Hz shall be traversed in approximately 1 minute. This motion shall be applied for a period of 2 hours in each 3 mutually perpendicular directions (total of 6 hours).
8.4 Temperature coefficient	Inductance change: Within ±20%	Temperature: -40 ~+125 With a reference value of +20 , change rate shall be calculated
8.5 Solderability	90% or more of electrode area shall be coated by new solder.	The test samples shall be dipped in flux, and then immersed in molten solder. Solder temperature: 245±5 Duration: 5±1 sec. Solder: Sn/3.0Ag/0.5Cu Flux: 25% resin and 75% ethanol in weight Immersion depth: all sides of mounting terminal shall be immersed
8.6 Resistance to Soldering Heat	No visible mechanical damage. Inductance change: Within ±10%	Re-flowing Profile: Please refer to Fig. 8.6-1. Test board thickness: 1.0mm Test board material: glass epoxy resin The chip shall be stabilized at normal condition for 1~2 hours before measuring 260 260 Max Ramp Up Rate=3 /sec. Max Ramp Down Rate=6 /sec. 60 - 90sec. 25 Time 25 to Peak =8 min max Fig. 8.6-1

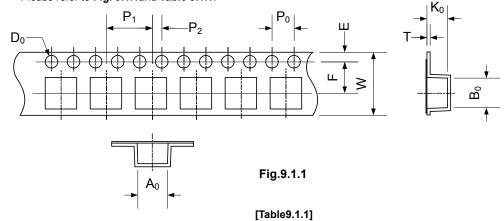
8.7	No visible mechanical damage.	Temperature and time: -40±3 for 30±3 min→125
Thermal Shock	Inductance change: Within ±10%	for 30±3min, please refer to Fig. 8.7-1.
	30 min. 30 min	Transforming interval: Max. 20 sec
	30 min. 30 min.	Tested cycle: 100 cycles
	Ambient	The chip shall be stabilized at normal condition for
	Temperature	1~2 hours before measuring
	20sec. (max.) Fig.8.7-1	
		Terrestere (0.0
8.8 Resistance to Low	No visible mechanical damage Inductance change: Within ±10%	Temperature: -40±3 Duration: 1000 ^{±24} hours
Temperature	inductance change. Within ±10%	The chip shall be stabilized at normal condition for
lemperature		1~2 hours before measuring
8.9	No mechanical damage.	Temperature:125±2
Resistance to High	Inductance change: Within ±10%	Duration: 1000 ^{±24} hours
Temperature		The chip shall be stabilized at normal condition for
		1~2 hours before measuring.
8.10	No mechanical damage.	Temperature: 60±2
Damp Heat	Inductance change: Within ±10%	Humidity: 90% to 95%RH
		Duration: 1000 ^{±24} hours
		The chip shall be stabilized at normal condition for
		1~2 hours before measuring
8.11	No mechanical damage.	Temperature: 60±2
Loading Under	Inductance change: Within ±10%	Humidity: 90% to 95% RH
Damp Heat		Applied current: Rated current
		Duration:1000 ^{±24} hours
		The chip shall be stabilized at normal condition for
8.12	No mechanical domago	1~2 hours before measuring
-	No mechanical damage. Inductance change: Within ±10%	Temperature: 85±2 Applied current: Rated current
Loading at High Temperature		Duration: 1000 ^{±24} hours
remperature		The chip shall be stabilized at normal condition for
		1~2 hours before measuring
		1 2 nouis before measuring

9 Packaging and Storage

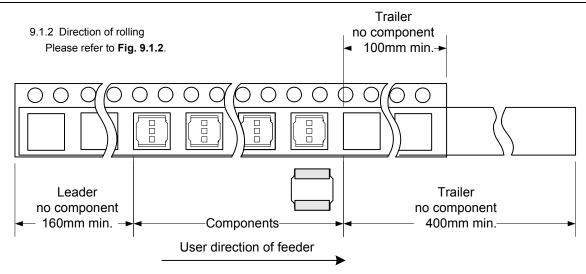
9.1 Tape and Reel Packaging Dimensions

9.1.1Taping Dimensions (Unit: mm)

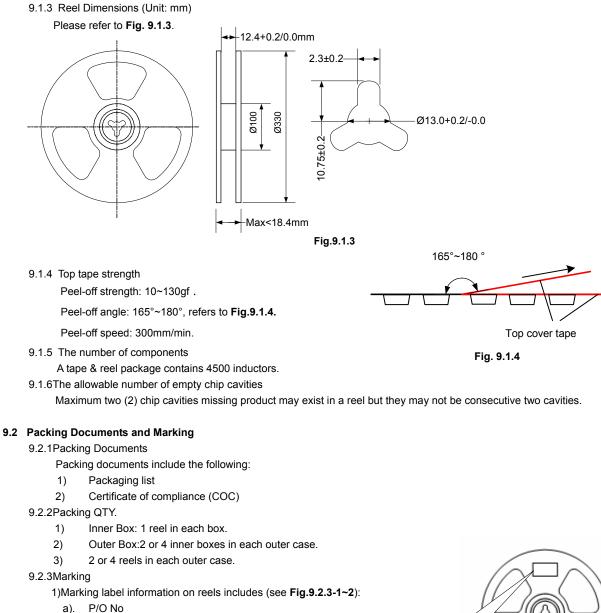
Please refer to Fig. 9.1.1 and Table 9.1.1.



Series	A ₀	B ₀	W	E	F	P ₀	P ₁	P ₂	D ₀	Т	K ₀
SWPA4012S	4.3±0.1	4.3±0.1	12.0±0.3	1.75±0.1	5.5±0.05	4.0±0.1	8.0±0.1	2.0±0.05	1.5+0.1/-0.0	0.35±0.03	1.6±0.1







- b). Customer Part No.
- c). Sunlord Part No.
- d). Quantity..
- e). Lot No.
- f). Date code
- g). Inspection stamp
- h). MFG address as 'Made In China'



See Fig.9.2.3-2

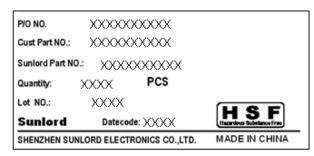


Fig.9.2.3-2

Inner box please refers to Fig.9.2.3-3 and Table 9.2.3-1

2)Marking label information on inner box

Marking Label on inner box

3)Marking on outer case (see Fig.9.2.3-4~6): Out case size pleases reefers to Table 9.2.3-2.

Manufacturer: Sunlord ID:

Customer

Date code C/No.

P/O No.

Customer Part No.

Sunlord Part No. Quantity.

Inspection Stamp.

Manufacturer

"Shenzhen Sunlord Electronics Co., Ltd." Packing label include the following:

Example; "1/10" means that this case is the 1st one

a). b).

a).

b).

i) ii)

iii)

iv)

v) vi)

vii)

viii)

ix)

Of total 10 cases

N/A

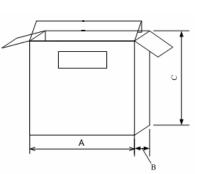


Fig.9.2.3-3

Packaging type	A(mm)	B(mm)	C(mm)	
Inner box	340	30	340	

[Table 9.2.3-1]

Packaging	L (mm)	W(mm)	H(mm)	
type	L(mm)	W(mm)		
Type1	380	380	250	
Type2	380	380	190	

[Table 9.2.3-2]

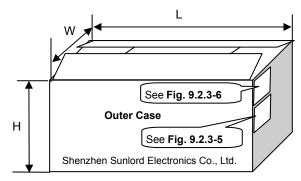


Fig. 9.2.3-4

SHENZHEN SUNLOR	D ELECTRONICS CO.,LTD.	MADE IN CHINA
Sunlord	Data code: XXXXXXXX	HSF
Quantity: xxxx	xxxx	PASS
Sunlord Part No: >	xx-xx-xxxx	
Cust Part No: XX)	QA	
P/O No: XXXXXX	KX	

Fig.9.2.3-5

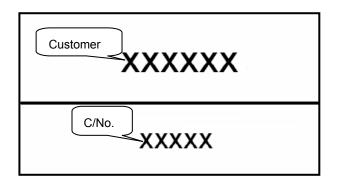


Fig.9.2.3-6

Sunlord

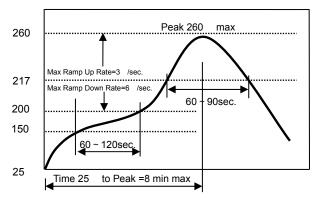
10 Visual inspection standard of product							
File No:	Applied to Wire Wound SMD Power Inductor Series						
Effective No.	e date: Defect Item	Graphic	Rejection identification	Acceptance			
1	Core defect		The defect length/width (I or <i>w</i>) more than L/6 or W/6, NG.	AQL=0.65			
2	Core crack		Visual cracks, NG.	AQL=0.65			
3	Starvation		Resin starved length, <i>I</i> , more than L/2, NG. IF <i>W</i> 2mm, resin starved width, <i>w</i> , more than W/2, NG. IF <i>W</i> 2mm, resin starved width, <i>w</i> , don't control.	AQL=0.65			
4	Excessive glue		The length, width or height of product beyond specified value, NG.	AQL=0.65			
5	Cold solder		For SWPA252012S, cold solders / more than 0.5 mm, NG. For other series, cold solders / more than 1 mm, NG.	AQL=0.65			
6	Solder icicle		The height H of product beyond specified value, NG; The clearance Δf beyond specified value listed in Item 5 , NG;	AQL=0.65			
7	Electrode uneven	h	The clearance Δf beyond specified value listed in Item 5 , NG;	AQL=0.65			
8	Marking defect		The content of marking 1) is indistinct, 2) disagrees with current product P/N requirements, NG; Intersection angle by L1 and L2 more than 45°, NG.	AQL=0.65			

11 Recommended Soldering Technologies

Sunlord

11.1Re-flowing Profile:
Preheat condition: 150 ~200 /60~120sec.
Allowed time above 217 : 60~90sec.
Max temp: 260
Max time at max temp: 5sec.
Solder paste: Sn/3.0Ag/0.5Cu
Allowed Reflow time: 2x max
Please refer to Fig. 11.1-1.

[Note: The reflow profile in the above table is only for qualification and is not meant to specify board assembly profiles. Actual board assembly profiles must be based on the customer's specific board design, solder paste and process, and should not exceed the parameters as the Reflow profile shows.]





11.2 Iron Soldering Profile Iron soldering power: Max. 30W

Pre-heating: 150 /60sec. Soldering Tip temperature: 350 Max. Soldering time: 3sec. Max. Solder paste: Sn/3.0Ag/0.5Cu Max.1 times for iron soldering Please refer to **Fig. 11.2-1**. [Note: Take care not to apply the tip of the soldering iron to the terminal electrodes.]

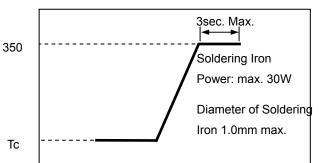


Fig. 11.2-1

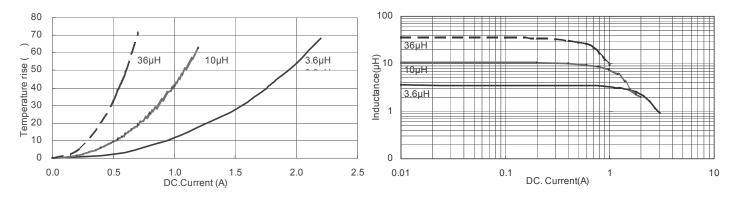
12 Electrical Characteristics

Part Number	Inductance	L Tolerance	Inductance Test Condition	DC Resistance (±30%)	Saturation Current	Temperature Rise Current	Min. Self-resonant frequency
Units	μH	-	-	Ω	А	А	MHz
Symbol	L	-	-	DCR	Isat	Irms	SRF
SWPA4012SR82NT	0.82	±30%	100K, 1V	0.050	3.02	1.65	150
SWPA4012S1R0NT	1.0	±30%	100K, 1V	0.050	2.61	1.65	120
SWPA4012S1R5NT	1.5	±30%	100K, 1V	0.065	2.10	1.46	90
SWPA4012S1R8NT	1.8	±30%	100K, 1V	0.080	2.12	1.32	88
SWPA4012S2R2NT	2.2	±30%	100K, 1V	0.080	1.76	1.32	74
SWPA4012S2R7NT	2.7	±30%	100K, 1V	0.090	1.90	1.25	71
SWPA4012S3R3NT	3.3	±30%	100K, 1V	0.110	1.72	1.12	60
SWPA4012S3R6NT	3.6	±30%	100K, 1V	0.110	1.20	1.12	57
SWPA4012S4R3NT	4.3	±30%	100K, 1V	0.140	1.58	1.00	54
SWPA4012S4R7NT	4.7	±30%	100K, 1V	0.125	1.15	1.05	50
SWPA4012S5R1NT	5.1	±30%	100K, 1V	0.155	1.55	0.95	50
SWPA4012S5R6NT	5.6	±30%	100K, 1V	0.140	1.00	1.00	42
SWPA4012S6R8MT	6.8	±20%	100K, 1V	0.198	0.85	0.84	40
SWPA4012S100MT	10	±20%	100K, 1V	0.265	0.80	0.77	33
SWPA4012S120MT	12	±20%	100K, 1V	0.290	0.66	0.70	32
SWPA4012S150MT	15	±20%	100K, 1V	0.340	0.56	0.64	25
SWPA4012S180MT	18	±20%	100K, 1V	0.470	0.55	0.55	23
SWPA4012S220MT	22	±20%	100K, 1V	0.587	0.46	0.49	20
SWPA4012S270MT	27	±20%	100K, 1V	0.720	0.50	0.45	18

Sunlord		Specification	Page 11 of 12				
SWPA4012S330MT	33	±20%	100K, 1V	0.810	0.42	0.42	17
SWPA4012S360MT	36	±20%	100K, 1V	0.900	0.40	0.40	14
SWPA4012S390MT	39	±20%	100K, 1V	1.100	0.55	0.37	16
SWPA4012S470MT	47	±20%	100K, 1V	1.100	0.35	0.37	12
SWPA4012S560MT	56	±20%	100K, 1V	1.250	0.33	0.33	11
SWPA4012S680MT	68	±20%	100K, 1V	1.950	0.38	0.27	11
SWPA4012S820MT	82	±20%	100K, 1V	2.140	0.28	0.26	11
SWPA4012S101MT	100	±20%	100K, 1V	2.210	0.25	0.25	9.4

Typical Electrical Characteristics

Inductance vs. DC Current Characteristics



13 Precautions

13.1 Surface mounting

- Mounting and soldering condition should be checked beforehand.
- Applicable soldering process to this product is reflow soldering only.
- Recommended conditions for repair by soldering iron:
 - Preheat the circuit board with product to repair at 150 for about 1 minute. Put soldering iron on the land-pattern.
 - Soldering iron's temperature: 350 maximum/Duration: 3 seconds maximum/1 time for each terminal.
 - The soldering iron should not directly touch the inductor.
 - Product once removes from the circuit board may not be used again.

13.2 Handing

- Keep the products away from all magnets and magnetic objects.
- Be careful not to subject the products to excessive mechanical shocks.
- Please avoid applying impact to the products after mounted on pc board.
- Avoid ultrasonic cleaning.

13.3 Storage

- To maintain the solderability of terminal electrodes and to keep the packing material in good condition, temperature and humidity in the storage area should be controlled.
- Recommended conditions: -10 ~40 , 70%RH (Max.)
- Even under ideal storage conditions, solderability of products electrodes may decrease as time passes. For this reason, product should be used with one year from the time of delivery.
- In case of storage over 6 months, solderability shall be checked before actual usage.

13.4 Regarding Regulations

- Any Class- or Class- ozone-depleting substance (ODS) listed in the Clean Air Act in US for regulation is not included in the products or applied to the products at any stage of whose manufacturing processes.
- Certain brominated flame retardants (PBBs,PBDEs) are not used at all.
- The products of this specification are not subject to the Export Trade Control Order in China or the Export Administration Regulations in US.

13.5 Guarantee

- The guaranteed operating conditions of the products are in accordance with the conditions specified in this specification.
- Please note that Sunlord takes no responsibility for any failure and/or abnormality which is caused by use under other than the aforesaid operating conditions.

14 Supplier Information

14.1 Supplier:

Shenzhen Sunlord Electronics Co., Ltd.

14.2 Manufacturer:

Shenzhen Sunlord Electronics Co., Ltd.

14.3 Manufacturing Address:

Sunlord Industrial Park, Dafuyuan Industrial Zone, Guanlan, Shenzhen, China Zip: 518110